

IN THE SPECIFICATION:

Page 32, under Fourth Embodiment, 2nd full paragraph.

In the differential input circuit according to the present embodiment, a ~~complete~~ full depletion type SOI (Silicon On Insulator) constitutes each circuit, which is different from the first embodiment.

Page 33, 3rd full paragraph

As Fig. 7 through Fig. 9 show, when a ~~complete~~ full depletion type SOI is used, the body region (i.e. the region corresponding to the substrate of the bulk CMOS structure) is set to the floating status.